

Thin Films Module Engineer

The Technology Development group's Thin Films division of Intel Corporation currently has openings for physical science and engineering Ph.D.s to support/direct R&D of advanced processing methods. Candidates hired for these positions will be responsible for developing the next generation of Intel's microprocessors. If you are considering a career which places you at the leading edge of technological innovation with the world's foremost semiconductor manufacturer, we encourage you to consider joining the team that is chartered with continuing to meet the challenge of Moore's Law in the 21st century.

Criteria for selection include for all candidates: a strong academic record, demonstrated experimental and data analysis expertise, superior critical thinking skills, and an ability to drive and to take responsibility for projects. Experience using and maintaining scientific equipment is valued. Semiconductor processing experience is not mandatory.

Openings are immediately available at Intel's primary development facility (Ronler Acres) located ~10 miles west of Portland, OR.

Interested candidates should email resumes to FE-Planar-Metals_Hiring@intel.com

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Intel Corporation

Job Description

Engineers within Intel's Logic Technology Development (LTD) organization are chartered with continuing to meet the challenge of Moore's Law in the 21st century. We innovate leading edge fabrication process and continue to overcome seemingly-impossible barriers as tolerances and specifications for building best-in-class computing devices become increasingly rigorous. Our world-class team consists of passionate, talented, accomplished scientists and engineers who hail from a diverse range of educational, cultural and technological backgrounds. We foster strong collaborations within groups of module and design engineers, as well as with equipment suppliers from all over the world. Our pursuit of increasingly efficient, powerful, tiny devices seeks to enable smarter, connected economies that enrich the lives of people around the globe.

The Logic Technology Development Thin Films Module Engineer's role encompasses ownership of critical thin films deposition and planarization techniques, enabling rapid miniaturization in the semiconductor industry and the mass production of integrated circuits. Duties of engineers in this role include:

Designing, executing and analyzing experiments necessary to meet engineering specifications for their process.

Participating in the development of intellectual property.

Developing the equipment necessary to exploit the understanding gained in research (in collaboration with equipment suppliers) and working effectively to identify shortcomings and propose and evaluate hardware modification to mitigate issues.

Overseeing their process in a manufacturing line which integrates the many individual steps necessary for the manufacture of complex microprocessors. This includes growing in-situ ramp to manufacturing volumes to demonstrate the technology meets requirements while simultaneously transferring the technology to counterparts in manufacturing sites across the globe.

This is an entry level position and will be compensated accordingly.

Qualifications

You must possess the below minimum qualifications to be initially considered for this position. Relevant experience can be obtained through school work, classes and project work, internships, military training, and/or work experience.

Minimum Requirements:

You must possess a Ph.D. degree majoring in Physics, Material Science, Chemistry, Chemical Engineering, Electrical Engineering, Mechanical Engineering, or a related field.

Preferred Qualifications:

Demonstrated experimental and data analysis expertise

Superior critical thinking skills and a strong academic record

Experience using and maintaining scientific equipment

Semiconductor processing experience is valued but by no means required

Inside this Business Group

As the world's largest chip manufacturer, Intel strives to make every facet of semiconductor manufacturing state-of-the-art -- from semiconductor process development and manufacturing, through yield improvement to packaging, final test and optimization, and world class Supply Chain and facilities support. Employees in the Technology and Manufacturing Group are part of a worldwide network of design, development, manufacturing, and assembly/test facilities, all focused on utilizing the power of Moore's Law to bring smart, connected devices to every person on Earth

Posting Statement

All qualified applicants will receive consideration for employment without regard to race, color, religion, religious creed, sex, national origin, ancestry, age, physical or mental disability, medical

condition, genetic information, military and veteran status, marital status, pregnancy, gender, gender expression, gender identity, sexual orientation, or any other characteristic protected by local law, regulation, or ordinance.